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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/087,779	03/05/2002	Thomas L. Johnson	1875.2050000	8819	
26111 7	590 10/03/2006		EXAM	INER	
STERNE, KESSLER, GOLDSTEIN & FOX PLLC			SEFCHECK, C	SEFCHECK, GREGORY B	
	ORK AVENUE, N.W. N, DC 20005		ART UNIT	PAPER NUMBER	
	,		2616		

DATE MAILED: 10/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			SE		
	Application No.	Applicant(s)	——————————————————————————————————————		
Advisory Action	10/087,779	JOHNSON ET AL.			
Before the Filing of an Appeal Brief	Examiner	Art Unit			
	Gregory B. Sefcheck	2616			
The MAILING DATE of this communication appe	ears on the cover sheet with the c	orrespondence add	ress		
THE REPLY FILED 22 September 2006 FAILS TO PLACE THI	S APPLICATION IN CONDITION F	OR ALLOWANCE.			
 The reply was filed after a final rejection, but prior to or on this application, applicant must timely file one of the follow places the application in condition for allowance; (2) a No a Request for Continued Examination (RCE) in compliance time periods: The period for reply expires 3 months from the mailing date 	wing replies: (1) an amendment, aff stice of Appeal (with appeal fee) in o se with 37 CFR 1.114. The reply mu	idavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)		
b) The period for reply expires on: (1) the mailing date of this A		in the final rejection, wh	ichever is later. In		
no event, however, will the statutory period for reply expire I Examiner Note: If box 1 is checked, check either box (a) or TWO MONTHS OF THE FINAL REJECTION. See MPEP 7	ater than SIX MONTHS from the mailin (b). ONLY CHECK BOX (b) WHEN THE	g date of the final rejecti	on.		
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee					
have been filed is the date for purposes of determining the period of ex under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b)	shortened statutory period for reply orig r than three months after the mailing da	inally set in the final Offi	ce action; or (2) as		
NOTICE OF APPEAL 2. The Notice of Appeal was filed on A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).					
AMENDMENTS					
 The proposed amendment(s) filed after a final rejection, (a) They raise new issues that would require further co (b) They raise the issue of new matter (see NOTE belo (c) They are not deemed to place the application in below 	nsideration and/or search (see NO w);	TE below);			
appeal; and/or (d) ☐ They present additional claims without canceling a	corresponding number of finally rej				
NOTE: (See 37 CFR 1.116 and 41.33(a)).			(DTOL 00.4)		
 4. The amendments are not in compliance with 37 CFR 1.1. 5. Applicant's reply has overcome the following rejection(s) 		mpliant Amendment ((PTOL-324).		
6. Newly proposed or amended claim(s) would be all non-allowable claim(s).		timely filed amendme	ent canceling the		
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is protected. The status of the claim(s) is (or will be) as follows:	will not be entered, or b) will will will will will will will	ll be entered and an e	explanation of		
Claim(s) allowed: Claim(s) objected to:					
Claim(s) rejected: <u>1-39</u> . Claim(s) withdrawn from consideration:					
AFFIDAVIT OR OTHER EVIDENCE					
8. The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good answas not earlier presented. See 37 CFR 1.116(e).					
9. The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to of showing a good and sufficient reasons why it is necessar	overcome <u>all</u> rejections under appea y and was not earlier presented. S	al and/or appellant fai ee 37 CFR 41.33(d)(ls to provide a 1).		
10. ☐ The affidavit or other evidence is entered. An explanatio REQUEST FOR RECONSIDERATION/OTHER		•			
11. The request for reconsideration has been considered bu <u>See Continuation Sheet.</u>	t does NOT place the application in	n condition for allowar	nce because:		
12. ☐ Note the attached Information Disclosure Statement(s).13. ☐ Other:	(PTO/SB/08) Paper No(s)				

9/28/06

Continuation of 11. does NOT place the application in condition for allowance because: Applicant's arguments are not convincing. The Examiner has reviewed all of the record and believes the rejections of claims 1-39 are proper.

Applicant contends that rejections of claims 1, 4, 5, 10-15, 18, 19 and 24-27 under 35 USC 102(e) are not proper because Carr discloses implementation through hardware, not software. Applicant further contends that the language of the claims would lead a person skilled in the art to refer to implementation through software.

The Examiner respectfully disagrees. Applicant's arguments regarding the interpretation of claim lanaguage as restricting the invention to software-implementation is most because Carr does, in fact, disclose implementation of the method in software.

Applicant cites Col. 12, lines 9-11 in Carr as showing that implementation is done exclusively through hardware. However, what is stated in this citation of Carr is that the elements of Fig. 3 are preferably implemented in hardware. This citation does not exclude the control of the disclosed method through software. Carr includes several disclosures of implementing/controlling the execution of the various functional blocks in Fig. 3 by processors, known by a person skilled in the art to operate by executing a computer program and, therefore, meets the contested claim limitations. See Col. 4, lines 60-65; Col. 10, lines 10-23; Col. 11, lines 1-27.

Further, the citation provided by Applicant does not explicitly show implementation in hardware only. The citation on Col. 12, lines 9-11 continues on lines 11-15 to state that the "integrated circuit" may include implementing logic on DRAM process, DRAM in a logic process, or a mixed process. As known to a person skilled in the art, "logic" and "process" are terms that denote a software application.

Seema S. Ras SEEMA S. RAO 9129106

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600